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Boundary Scan Software Platform SYSTEM CASCON™ revolutionises interactive graphical Hardware Verification

Jena / Birmingham – During NEPCON UK in Birmingham, GOEPEL electronic, world-class vendor of JTAG/Boundary Scan solutions compliant with IEEE Std. 1149.x, introduces a new generation of intelligent tools for interactive hardware verification named ScanAssist™ as part of the Boundary Scan software platform SYSTEM CASCON™.

The new tools enable the real-time activation and analysis of individually defined logic states directly in the target hardware (Pin/Net Toggler) without prior test program generation. They feature an unrivalled intelligence, and automatically identify potentially unsafe or hardware harming vectors as well as logic states of non-Boundary Scan nets and pins (cluster logic). The new tool suite is complemented by next generation interactive graphical visualizer SCAN VISION™ III, which has been developed with ASTER Technologies within the frame of an OEM agreement. In addition to the native schematic and layout cross probing, this tool offers the innovation to apply a data flow oriented, virtual schematic for interactive logic probing in a completely integrated development environment.

"With the new tools we're the first vendor to close the gap between the requirements to graphically analyse logic states of the target hardware fast and flexible. Additionally, it's now possible to eliminate target hardware damages by faulty vectors and to involve non-Boundary Scan circuit parts into the verification at the same time", Thomas Wenzel, Managing Director Boundary Scan Division at GOEPEL electronic gladly announces. "Based on the tight cooperation with ASTER Technologies, we're now able to enormously enhance our second generation Boundary Scan visualization capability. The entire solution sets new patterns in particular for a fast, flexible and safe prototype hardware verification (Rapid Prototype Test)".

Christophe Lotz, Managing Director of ASTER Technologies said: "We are pleased to be associated with Goepel Electronics and have the opportunity to demonstrate the seamless integration of our advanced board viewing technology within SCAN VISION™ III. It is a clear indication of ASTER's continued commitment to work closely with the industry's leading test equipment suppliers."

The new ScanAssist™ Interactive Pin Toggler complements the existing ScanAssist™ Multi Mode Debugger building a complete debug tool suite.

The new tool uses the central data base of SYSTEM CASCON, and in active state initialises all Boundary Scan structures and administrates the test vectors. That means, the user doesn't need any knowledge about specific TAP control sequences and operation modi, and can concentrate on verification tasks. Logic levels can be provided in object browsers and ASCII files as well as in schematic and Layout Visualizer.

The ScanAssist™ interactive pin toggler can reach an especially high performance in combination with the innovative SCAN VISISON™ III Visualizers, which can be completely integrated. Besides standard functions they enable a net and pin oriented colour presentation of logic states and fault states. The Virtual Schematic principle enables the synthetic combination of native schematic sectors, and therefore, improves the debug process efficiency. A tracing of the entire debug session is possible because all user interactions are recorded separately. Single toggles can be summarised in groups and put out in vector forms. Additionally, there's the option to export single or all recorded vectors as CASLAN test program. In this context, the ScanAssist™ Inteactive Pin-Toggler can be utilised as tool for interactive test program generation.

ScanAssist™ and SCANVISISON™ III will be integrational part in SYSTEM CASCON™ from Version 4.4 on, activated per license manager. This release will be provided in the early third quarter 2007, and will be free of charge for all users with valid software maintenance contract. All tools are compatible with all Boundary Scan controllers based on PCI, USB2.0, LAN, PCIe, PXI, PXIe, VXI, Fire Wire, the product lines SCANFLEX® and ScanBooster™.

GOEPEL electronic, founded in 1991 and headquartered in Jena/Germany, is a worldwide leading vendor of innovative JTAG / Boundary Scan / IEEE 1149.x solutions, offering mature software tools in an integrated development environment, high-performance Boundary Scan controllers and accessories, as well as comprehensive product support and value added services. The company of about 125 employees generated a revenue of more than 16 million EURO in 2006, maintaining support and sales offices in Germany, France, the United Kingdom,

and the United States. An extended distribution and service network of over 300 specialists ensures excellent local and on-site customer support for more than 4,500 worldwide system installations. GOEPEL electronic has continuously been ISO9001 certified since 1996 and has been honoured with TOP-JOB and TOP-100 awards for being one of the best medium-sized companies in Germany. GOEPEL electronic's products won several awards in recent years and are used by the leading companies in telecommunication, automotive, space and avionics, industrial controls, medical technology, and other industries. Further information about the three time "Best-in-Test Award" winner can be found on the Internet at www.goepel.com.

ASTER Technologies is the leading supplier in Board-Level Testability analysis tools which capitalizes on proven expertise in board testability, developed through strong customer relations. Founded in 1993, ASTER has developed a wide range of products dealing with electronic PCB Testability, Visualization and Quality Management.

Our skilled employees constantly develop and enhance a suite of complementary products, while our global network of specialists support our worldwide installed base, including independent support centers, distributors and value added resellers. For more information about the company and its solutions, please visit www.aster-technologies.com

Contact:
GOEPEL electronic GmbH
Stefan Meissner
Göschwitzer Str. 58-60
07745 Jena/Germany
Tel.: +49-3641-6896-739
Fax: +49-3641-6896-744
E-Mail: s.meissner@goepel.com
Internet: http://www.goepel.com